

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): ITO et al.	Atty. Dkt.: 01-149
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THE SAME	

J1017 U.S. PTO  
09/865704  
05/29/01

Assistant Commissioner for Patents  
Washington, D.C. 20231

Date: May 29, 2001

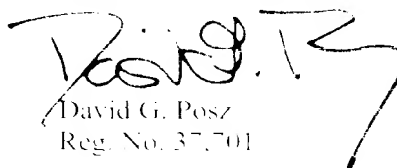
INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is are being submitted for consideration by the Examiner without any admission that it they constitute(s) statutory prior art, or without any admission that it they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has have been considered and made of record.

Respectfully submitted,

  
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